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CONFIRMATION NO. 3430

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| RULE | | | | |
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| ** CONTINUING DATA ***** This application is a 371 of PCT/JP04/15405 10/19/2004 | | | | |
| ** FOREIGN APPLICATIONS ***** JAPAN 2003-359262 10/20/2003 JAPAN 2004-294133 10/06/2004 | | | | |
| ** IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 12/23/2006 | | | | |
| Foreign Priority claimed <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No 35 USC 119(a-d) conditions met <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No Verified and Acknowledged <u>/ALLAN R WILSON/</u> <small>Examiner's signature</small> | <input type="checkbox"/> Met after Allowance <small>Indicate</small> | STATE OR COUNTRY JAPAN | SHEETS DRAWINGS 25 | TOTAL CLAIMS 22 |
| INDEPENDENT CLAIMS 9 | | | | |
| ADDRESS FOLEY AND LARDNER LLP SUITE 500 3000 K STREET NW WASHINGTON, DC 20007 UNITED STATES | | | | |
| TITLE Semiconductor device and manufacturing process therefor | | | | |
| FILING FEE RECEIVED 3310 | FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following: | | <input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit | |